

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,068,544 B2
APPLICATION NO. : 09/945507
DATED : June 27, 2006
INVENTOR(S) : Forbes et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 1, line 20, after "Insulators,"* insert -- attorney docket no.
(Micron 01-0485), --.

In column 2, line 55, delete "DEViCES" and insert -- DEVICES --, therefor.

In column 4, line 6, delete "an" and insert -- a --, therefor.

In column 4, line 36, delete "horizonal" and insert -- horizontal --, therefor.

In column 5, line 58, delete "transistors," and insert -- transistors--, therefor.

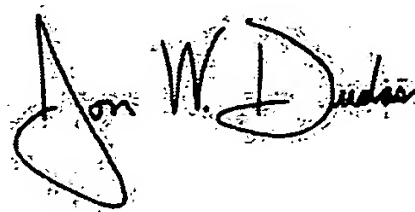
In column 7, line 38, delete "adresses" and insert -- addresses --, therefor.

In column 19, line 58, in Claim 24, delete "integrate" and insert -- intergate --, therefor.

In column 22, line 26, in Claim 43, delete "fanned" and insert -- formed --, therefor.

Signed and Sealed this

Sixth Day of March, 2007

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office